TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG17AFS

Schmitt Buffer

Features

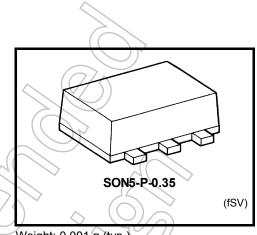
High output current : ± 8 mA (min) at $V_{CC} = 3.0$ V

Super high speed operation : $t_{pd} = 3.7 \text{ ns (typ.)}$

at $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$

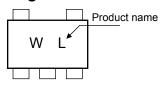
Operating voltage range : V_{CC} = 0.9 to 3.6 V

5.5-V tolerant input.

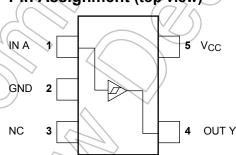


Weight: 0.001 g (typ.)

Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	-0.5 to 4.6	٧
DC input voltage	V _{IN}	-0.5 to 7.0	٧
DC output voltage	Vout	-0.5 to V _{CC} + 0.5	>
Input diode current	∠lik	-20	mA
Output diode current	lok	±20 (Note 1)	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Tcc	±50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	−65 to 150	°C

Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: V_{OUT} < GND, V_{OUT} > V_{CC}

Start of commercial production 2005-07

IEC Logic Symbol

Truth Table



А	Υ
L	L
Н	Н

Operating Ranges

Characteristics	Symbol	Rating
Supply voltage	V _{CC}	0.9 to 3.6
Input voltage	V _{IN}	0 to 5.5
Output voltage	V _{OUT}	0 to V _{CC} V
Output Current	I _{OH} /I _{OL}	±8.0 (Note 2) ±4.0 (Note 3) ±3.0 (Note 4) ±1.7 (Note 5) ±0.3 (Note 6) ±0.02 (Note 7)
Operating temperature	T _{opr}	-40 to 85 °C

Note 2: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 3: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 4: $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$

Note 5: $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$

Note 6: $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$

Note 7: $V_{CC} = 0.9 V$



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Electrical Characteristics

DC Characteristics

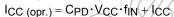
Characteristics Sym		Symbol Test Condition				Ta = 25°C			Ta = -40 to 85°C		Unit
		Cymbol	Test Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
					0.9	_	_ <	0.73		0.80	
Positive					1.1	_	_	0.86	1	0.93	
	.,			1.4	_	_	1.07)/_	1.12		
	threshold voltage	V _P		_	1.65	_	+0	1.23	_	1.25	
							1	1.66	_	1.68	
Threshold					3.0	-(7	2.14	-	2.15	
voltage					0.9	0.18		_	0.07	_	V
					1.1	0.26	_	_	0.18	_	
	Negative threshold	V			1.4	0.36	_	_ /	0.31		
	voltage	V _N		_	1.65	0.45		-6	0.41	> _	
					2.3	0.69	_0	7-6	0.64) —	
					3.0	0.96			0.91	_	
				A	0.9	0.20	-((0.38	0.15	0.53	
		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \			1.1	0.25		0.41	0.21	0.53	
Hysteresis volta	ane			1.4	0.35		0.48	0.34	0.57	V	
Trysteresis voite	age	VН	V _H	_ < </td <td>1.65</td> <td>0.42</td> <td></td> <td>0.56</td> <td>0.40</td> <td>0.60</td> <td>V</td>	1.65	0.42		0.56	0.40	0.60	V
				2.3	0.60	//-	0.74	0.59	0.76		
				3.0	0.79	//-	0.93	0.78	0.94		
				I _{OH} =-0.02 mA	0.9	0.75	_	_	0.75	1	
				I _{ОН} = -0.3 mA	1.1 to 1.3	V _{CC} × 0.75	_	_	V _{CC} × 0.75		
High level	VOH VII	V _{IN} =V _{IH}	I _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	_	_	V _{CC} × 0.75			
				I _{OH} = −3.0 mA	1.65 to 1.95	V _{CC} -0.45	_	_	V _{CC} -0.45		
		~<		$I_{OH} = -4.0 \text{ mA}$	2.3 to 2.7	2.0	_	_	2.0		
Output voltage				$I_{OH} = -8.0 \text{ mA}$	3.0 to 3.6	2.48	_	_	2.48	1	V
voltage	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	\$\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		$I_{OL} = 0.02 \text{ mA}$	0.9	_	_	0.1	_	0.1	
		9	\langle	I _{OL} = 0.3 mA	1.1 to 1.3	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	
	Low level	V _{OL}	VIN=VIL	loL ⇒ 1.7 mA	1.4 to 1.6	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	
	-	((I _{OL} = 3.0 mA	1.65 to 1.95	_	_	0.45	_	0.45	
		7		I _{OL} = 4.0 mA	2.3 to 2.7	_	_	0.4	_	0.4	
				I _{OL} = 8.0 mA	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage current I _{IN} V		V _{IN} = 0 to 5.5V		0 to 3.6	_	_	±0.1	_	±1.0	μА	
Quiescent supply current I _{CC} V _{IN} = V _{CC} or GNI		or GND	3.6	_	_	1.0	_	10.0	μА		

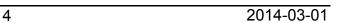
AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	oot Condition		Ta = 25°C			Ta = -40 to 85°C	
Characteristics	Symbol	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	^t pLH ^t pHL	C_L = 10 pF, R _L = 1 M Ω	0.9	_	27.3	_	_	_	ns
			1.1 to 1.3	_	13.0	22.6	1.0	35.9	
			1.4 to 1.6		7.5	10.5	1.0	11.3	
			1.65 to 1.95		6.0	7.8	1,0	8.2	
			2.3 to 2.7		4.3	5.4	1.0	5.8	
			3.0 to 3.6	- <	3.5	4.4	1.0	4.6	
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		29.5)))	_		
			1.1 to 1.3		14.3	25.1	1.0	41.8	
			1.4 to 1.6	<u></u>) 8.	11.5	1.0	12.6	
			1.65 to 1.95	A.	6,3	8.4	1.0	8.7	
			2.3 to 2.7	4	4.6	5.7	1.0	6.1	
			3.0 to 3.6	//-	3.7	4.6	1).0	5.0	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9))	40.5		74/	/ —	
			1.1 to 1.3	<u> </u>	19.6	35.7	1.0	58.1	
			1.4 to 1.6		10.7	15.8	1.0	17.6	
			1.65 to 1.95		7.8	10.7	1.0	11.7	
			2.3 to 2.7		5.4	6.9	1.0	8.1	
			3.0 to 3.6		4.3	5.2	1.0	6.1	
Input capacitance	C _{IN}		3.6	/	3		_	_	pF
Power dissipation capacitance	C _{PD}	(Note 8)	0.9 to 3.6		1/7	_	_	_	pF

Note 8: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

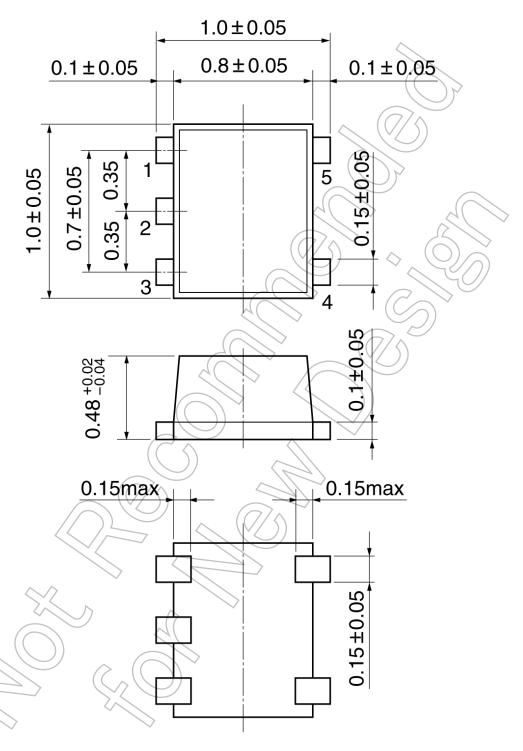
Average operating current can be obtained by the equation:





Package Dimensions

SON5-P-0.35 Unit: mm



Weight: 0.001 g (typ.)

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