

### Features

- Both the DTA114E Chip and DTC114E Chip In a Package
- Mounting Possible With SOT-363 Automatic Mounting Machines
- Transistor Elements Independent, Eliminating Interference
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

### Maximum Ratings @ 25°C Unless Otherwise Specified

#### DTR1

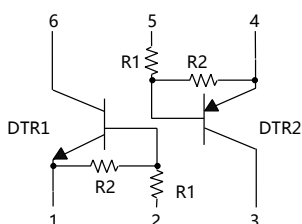
Parameter	Symbol	Value	Unit
Supply Voltage	$V_{CC}$	50	V
Input Voltage	$V_{IN}$	-10~40	V
Output Current	$I_O$	50	mA
Peak Collector Current	$I_{CM}$	100	mA
Power Dissipation	$P_D$	150	mW
Junction Temperature	$T_J$	150	°C
Storage Temperature	$T_{stg}$	-55~150	°C

#### DTR2

Parameter	Symbol	Min	Typ	Max	Unit
Supply Voltage	$V_{CC}$	---	-50	---	V
Input Voltage	$V_{IN}$	-40	---	10	V
Collector Current	$I_C$	---	-50	-100	mA
Power Dissipation	$P_D$	---	150	---	mW
Junction Temperature	$T_J$	---	---	150	°C
Storage Temperature	$T_{stg}$	-55	---	150	°C

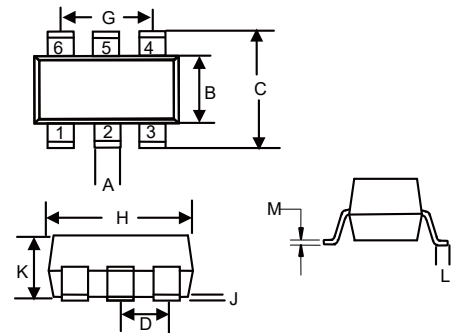
### Device Marking: D3

#### Internal Structure



## NPN&PNP Digital Transistor

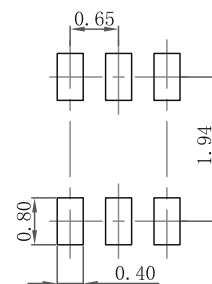
### SOT-363



#### DIMENSIONS

DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.006	0.014	0.15	0.35	
B	0.045	0.053	1.15	1.35	
C	0.079	0.096	2.00	2.45	
D	0.026		0.65		TYP.
G	0.047	0.055	1.20	1.40	
H	0.071	0.087	1.80	2.20	
J	-----	0.004	-----	0.10	
K	0.031	0.043	0.80	1.10	
L	0.010	0.018	0.26	0.46	
M	0.003	0.006	0.08	0.15	

#### Suggested Solder Pad Layout



**Electrical Characteristics @ 25°C Unless Otherwise Specified**
**DTR1 NPN**

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Input Voltage	$V_{I(off)}$	0.5	---	---	V	$V_{CC}=5V, I_O=100\mu A$
	$V_{I(on)}$	---	---	3.0	V	$V_O=0.3V, I_O=10mA$
Output Voltage	$V_{O(on)}$	---	---	0.3	V	$I_O=10mA, I_I=0.5mA$
Input Current	$I_I$	---	---	0.88	mA	$V_I=5V$
Output Current	$I_{O(off)}$	---	---	0.5	$\mu A$	$V_{CC}=50V, V_I=0$
DC Current Gain	$G_1$	30	---	---		$V_O=5V, I_O=5mA$
Input Resistance	$R_1$	7	10	13	K $\Omega$	
Resistance Ratio	$R_2/R_1$	0.8	1.0	1.2		
Transition Frequency	$f_T$	---	250	---	MHz	$V_{CE}=10V, I_E=-5mA, f=100MHz$

**DTR2 PNP**

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Input Voltage	$V_{I(off)}$	-0.5	---	---	V	$V_{CC}=-5V, I_O=-100\mu A$
	$V_{I(on)}$	---	---	-3.0	V	$V_O=-0.3V, I_O=-10mA$
Output Voltage	$V_{O(on)}$	---	---	-0.3	V	$I_O=-10mA, I_I=-0.5mA$
Input Current	$I_I$	---	---	-0.88	mA	$V_I=-5V$
Output Current	$I_{O(off)}$	---	---	-0.5	$\mu A$	$V_{CC}=-50V, V_I=0$
DC Current Gain	$G_1$	30	---	---		$V_O=-5V, I_O=-5mA$
Input Resistance	$R_1$	7.0	10	13	K $\Omega$	
Resistance Ratio	$R_2/R_1$	0.8	1.0	1.2		
Transition Frequency	$f_T$	---	250	---	MHz	$V_{CE}=-10V, I_E=5mA, f=100MHz$

Curve Characteristics

Fig. 1 - DTR1 DC Current Gain Characteristics

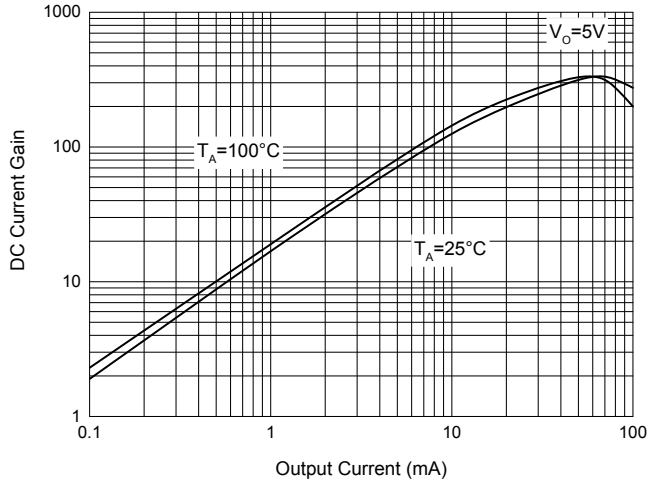


Fig. 2 - DTR1 Input Voltage (on) Characteristics

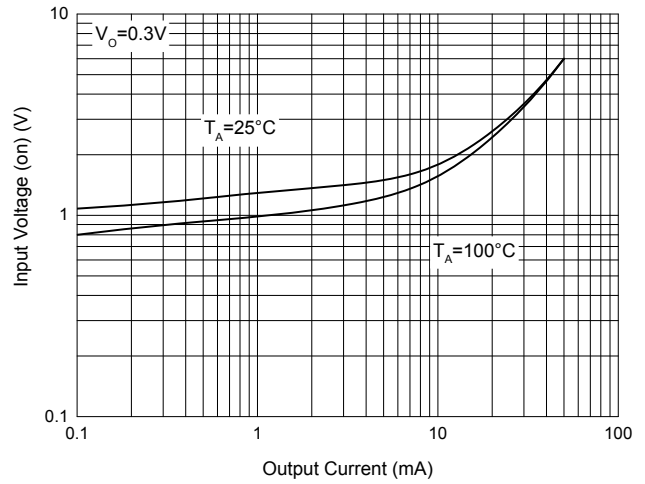


Fig. 3 - DTR1 Input Voltage (off) Characteristics

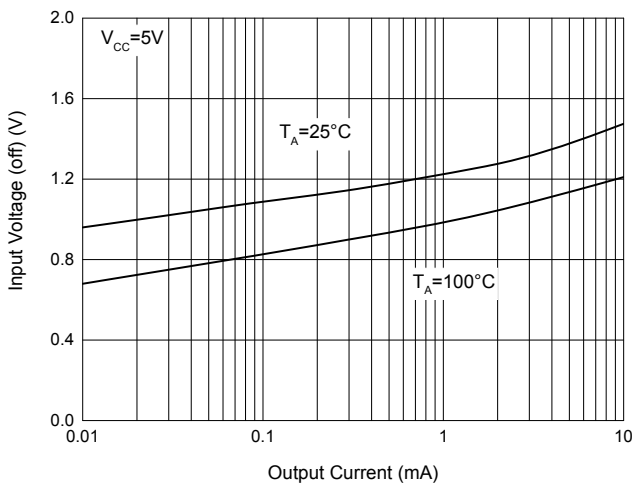


Fig. 4 - DTR1 Output Voltage Characteristics

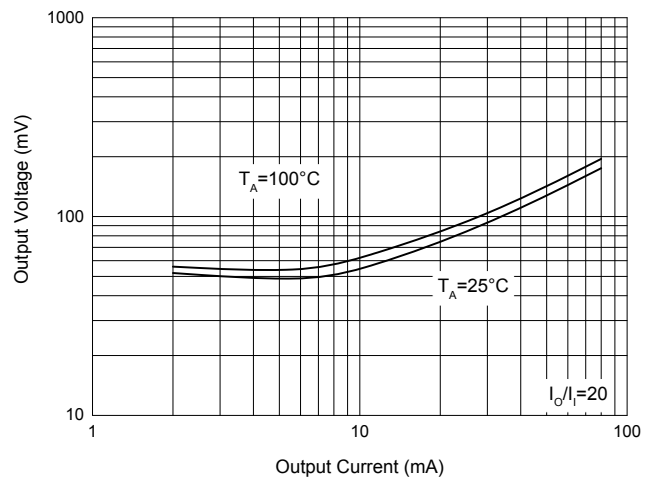


Fig. 5 - DTR2 DC Current Gain Characteristics

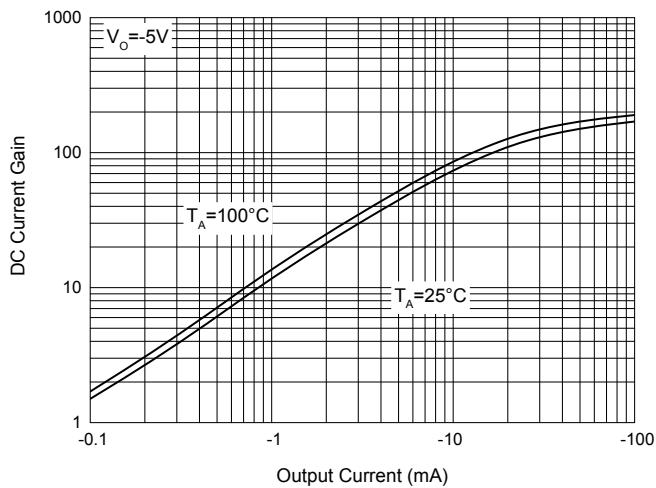
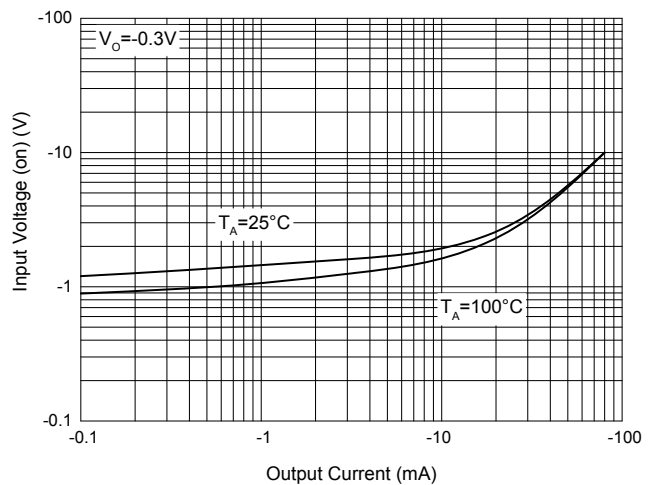


Fig. 6 - DTR2 Input Voltage (on) Characteristics



Curve Characteristics

Fig. 7 - DTR2 Input Voltage (off) Characteristics

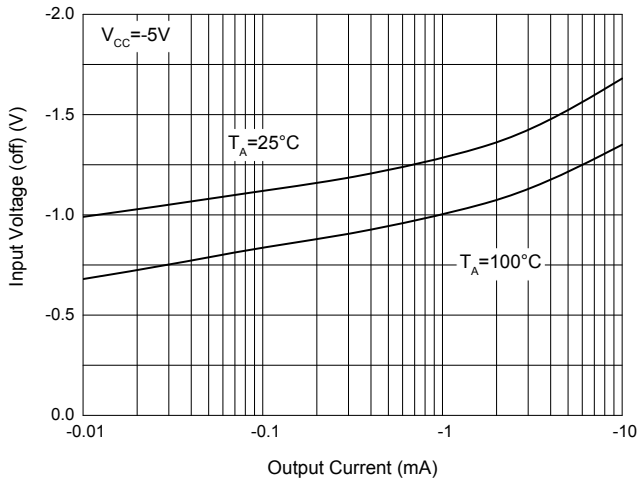


Fig. 8 - DTR2 Output Voltage Characteristics

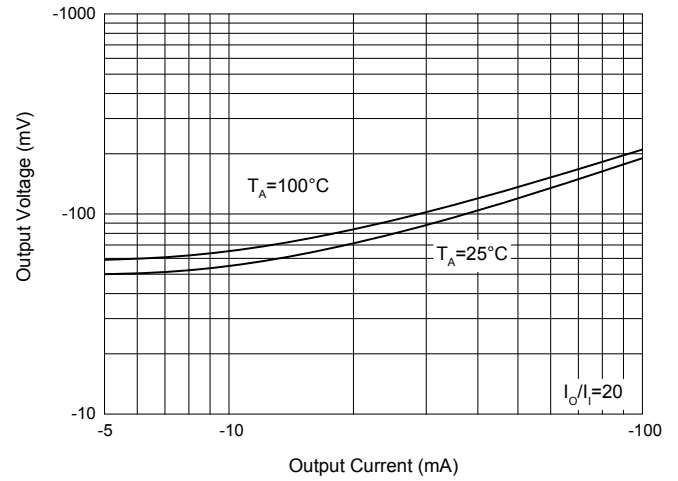
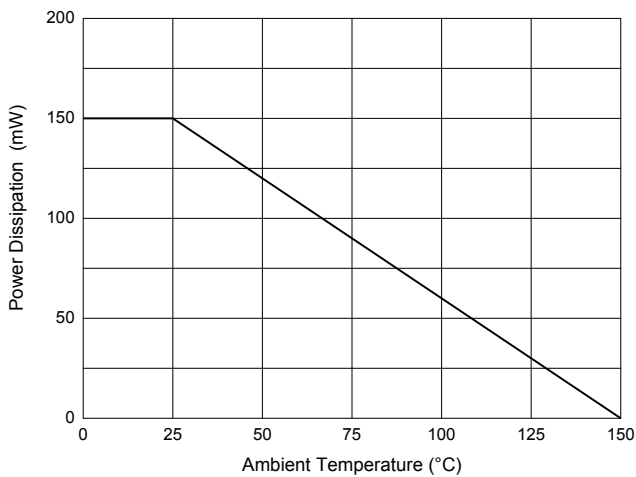


Fig. 9 - Power Derating Curve



## Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

Note : Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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