

PNP PRE-BIASED SMALL SIGNAL DUAL SURFACE MOUNT TRANSISTOR

Features

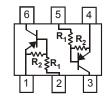
- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDC)
- Built-In Biasing Resistors
- Available in Lead Free/RoHS Compliant Version (Note 3)

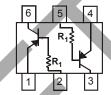
Part Number	R1	R2	Marking
DDA124EK	22K Ω	22K Ω	P17
DDA144EK	47ΚΩ	47ΚΩ	P20
DDA114YK	10KΩ	47ΚΩ	P14
DDA123JK	2.2ΚΩ	47ΚΩ	P06
DDA114EK	10KΩ	10KΩ	P13
DDA143TK	4.7ΚΩ	-	P07
DDA114TK	10 K Ω	-	P12

Mechanical Data

- Case: SOT-26
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: See Diagram
- Terminals: Solderable per MIL-STD-202, Method 208
- Also Available in Lead Free Plating (Matte Tin Finish annealed over Copper leadframe). Please see Ordering Information, Note 5, on Page 5
- Marking Information: See Table and Page 5
- Ordering Information See Page 5
- Weight: 0.015 grams (approximate)







Top View

R1, R2 Device Schematic

R1 only Device Schematic

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit
Supply Voltage, (1) to (6) and (4) to (3)		Vcc	50	V
Input Voltage, (2) to (1) and (5) to (4)	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK DDA143TK DDA114TK	V _{IN}	+10 to -40 +10 to -40 +6 to -40 +5 to -12 +10 to -40 +5V max +5V max	V
Output Current	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK DDA143TK DDA114TK	lo	-30 -30 -70 -100 -50 -100	mA
Output Current	All	I _{C(MAX)}	-100	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Total)	P_{D}	300	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{ hetaJA}$	416.7	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes:

- 1. Mounted on FR4 PC Board with recommended pad layout at http://www.diodes.com/datasheets/ap02001.pdf.
- 2. 200mW per element must not be exceeded.
- 3. No purposefully added lead.



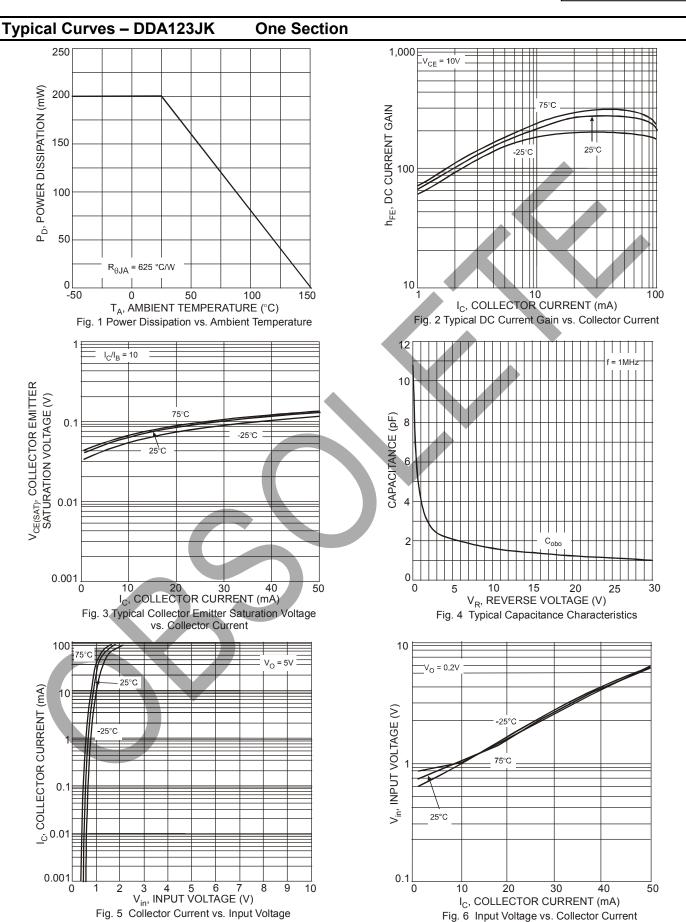
Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic (DDA143TK & DDA114TK only)	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	-50	_	_	V	$I_{C} = -50 \mu A$
Collector-Emitter Breakdown Voltage	BV _{CEO}	-50	_	_	V	$I_C = -1mA$
Emitter-Base Breakdown Voltage	BV _{EBO}	-5	_	_	V	$I_E = -50 \mu A$
Collector Cutoff Current	I _{CBO}	_	_	-0.5	μΑ	V _{CB} = -50V
Emitter Cutoff Current	I _{EBO}	_	_	-0.5	μΑ	V _{EB} = -4V
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	_	_	-0.3	V	$I_C/I_B = -2.5 \text{mA} / -0.25 \text{mA}$ DDA143TK $I_C/I_B = -1 \text{mA} / -0.1 \text{mA}$ DDA114TK
DC Current Transfer Ratio	h _{FE}	100	250	600		$I_C = -1 \text{mA}, V_{CE} = -5 \text{V}$
Input Resistor (R ₁) Tolerance	ΔR_1	-30	_	+30	%	_
Gain-Bandwidth Product*	f _T	—	250		MHz	$V_{CE} = -10V$, $I_{E} = 5mA$, $f = 100MHz$

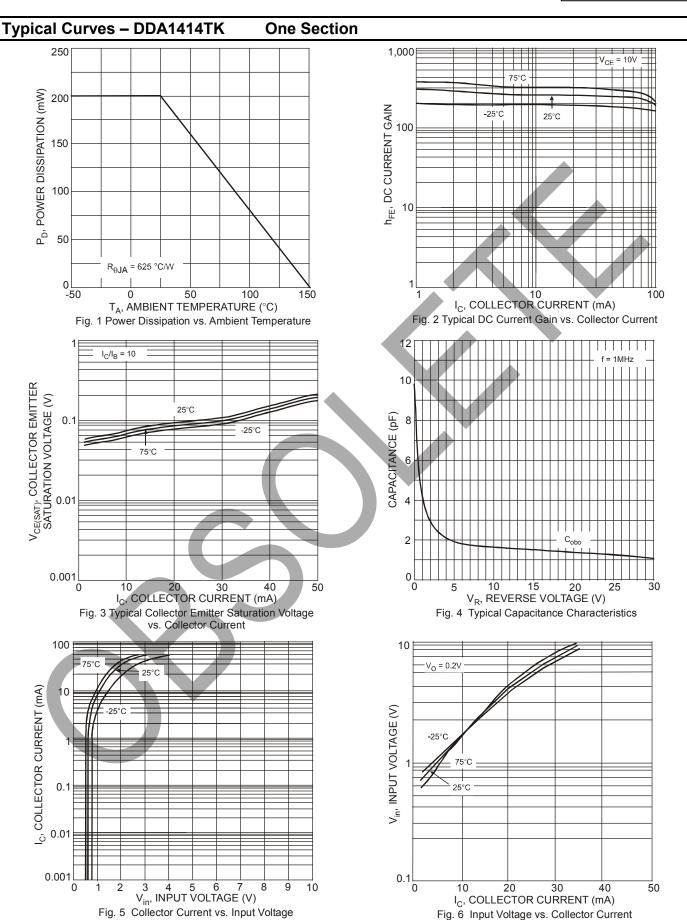
Ch ava ataviat	<u> </u>	Complete	N4:	T	Marr	11	Test Condition
Characterist		Symbol	Min	Тур	Max	Unit	Test Condition
	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK	VI(OFF)	-0.5 -0.5 -0.3 -0.5 -0.5	-1.1 -1.1 — — -1.1	_	.,	V _{CC} = -5V, I _O = -100μA
Input Voltage	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK	V _{I(ON)}	l	-1.9 -1.9 — — -1.9	-3.0 -3.0 -1.4 -1.1 -3.0	>	$V_O = -0.3$, $I_O = -5mA$ $V_O = -0.3$, $I_O = -2mA$ $V_O = -0.3$, $I_O = -1mA$ $V_O = -0.3$, $I_O = -5mA$ $V_O = -0.3$, $I_O = -10mA$
Output Voltage	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK	V _{O(ON)}		-0.1	-0.3	V	I _O /I _I = -10mA / -0.5mA I _O /I _I = -10mA / -0.5mA I _O /I _I = -5mA / -0.25mA I _O /I _I = -5mA / -0.25mA I _O /I _I = -10mA / -0.5mA
Input Current	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK	I _I		l	-0.36 -0.18 -0.88 -3.6 -0.88	mA	V ₁ = -5V
Output Current		I _{O(OFF)}	_	_	-0.5	μΑ	$V_{CC} = 50V, V_I = 0V$
DC Current Gain	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK	Gı	56 68 68 80 30	_	_	_	$V_O = -5V$, $I_O = -5mA$ $V_O = -5V$, $I_O = -5mA$ $V_O = -5V$, $I_O = -10mA$ $V_O = -5V$, $I_O = -10mA$ $V_O = -5V$, $I_O = -5mA$
Input Resistor (R ₁) Tolerance	•	ΔR_1	-30	_	+30	%	_
Resistance Ratio Tolerance		R ₂ /R ₁	-20	_	+20	%	_
Gain-Bandwidth Product*	f _T	_	250	_	MHz	V _{CE} = -10V, I _E = -5mA, f = 100MHz	

^{*} Transistor - For Reference Only











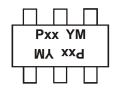
Ordering Information (Notes 4 & 5)

Part Number	Case	Packaging
DDA124EK-7	SOT-26	3000/Tape & Reel
DDA144EK-7	SOT-26	3000/Tape & Reel
DDA114YK-7	SOT-26	3000/Tape & Reel
DDA123JK-7	SOT-26	3000/Tape & Reel
DDA114EK-7	SOT-26	3000/Tape & Reel
DDA143TK-7	SOT-26	3000/Tape & Reel
DDA114TK-7	SOT-26	3000/Tape & Reel

Notes:

4. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.
5. For Lead Free/RoHS Compliant version part numbers, please add "-F" suffix to the part numbers above. Example: DDA114TK-7-F.

Marking Information



Pxx = Product Type Marking Code (See Page 1)

YM = Date Code Marking

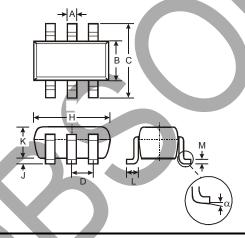
Y = Year (ex: T = 2006)

M = Month (ex: 9 = September)

Date Code Kev

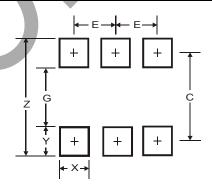
Year	2006	2007	200	8 2	2009	2010	2011	201	2 20	13	2014	2015
Code	T	U	V		W	Х	Υ	Z		Ă	В	С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Package Outline Dimensions



SOT-26							
Dim	Min	Max	Тур				
Α	0.35	0.50	0.38				
В	1.50	1.70	1.60				
С	2.70	3.00	2.80				
D	_	_	0.95				
Н	2.90	3.10	3.00				
J	0.013	0.10	0.05				
K	1.00	1.30	1.10				
L	0.35	0.55	0.40				
М	0.10	0.20	0.15				
α	0°	8°	_				
All D	imensi	ons in	mm				

Suggested Pad Layout



Dimensions	Value (in mm)
Z	3.20
G	1.60
X	0.55
Y	0.80
С	2.40
E	0.95



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