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# RENESAS HD74LVC138

# 3-to-8-line Decoder / Demultiplexer

REJ03D0349-0300Z (Previous ADE-205-068B (Z)) Rev.3.00 Jul. 23, 2004

### Description

The HD74LVC138 has three binary select inputs in a 16 pin package. If the device is enabled these inputs determine which one of the eight normally high outputs will go low. Two active low and one active high enables are provided to ease the cascading of decoders. Low voltage and high-speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

### Features

- $V_{CC} = 2.0 \text{ V to } 5.5 \text{ V}$
- All inputs  $V_{IH}$  (Max.) = 5.5 V (@V<sub>CC</sub> = 0 V to 5.5 V)
- Typical  $V_{OL}$  ground bounce < 0.8 V (@V<sub>CC</sub> = 3.3 V, Ta = 25°C)
- Typical  $V_{OH}$  undershoot > 2.0 V (@V<sub>CC</sub> = 3.3 V, Ta = 25°C)
- High output current  $\pm 24 \text{ mA}$  (@V<sub>CC</sub> = 3.0 V to 5.5 V).
- Ordering Information

Part Name	rt Name Package Type		Package Abbreviation	Taping Abbreviation (Quantity)		
HD74LVC138FPEL	SOP–16 pin (JEITA)	FP–16DAV	FP	EL (2,000 pcs/reel)		
HD74LVC138TELL	TSSOP-16 pin	TTP-16DAV	Т	ELL (2,000 pcs/reel)		

Note: Please consult the sales office for the above package availability.

### **Function Table**

#### Inputs Outputs Enable Select G<sub>1</sub> С $Y_2$ $Y_3$ $Y_4$ $Y_6$ $Y_7$ G<sub>2A</sub> G<sub>2B</sub> B A $Y_0$ Y₁ $Y_5$ Х Х Н Х X Х Н Н н Н н Н н н Х Н Х Х Х Н Н Х Н Н Н Н н Н L Х Х Х Х Х Н Н Н Н Н Н Н Н Н L Н Н Н Н Н L L L L L Н Н Н L Н Н Н L L L Н L Н Н Н Н Η Н н Н Н Н Н L L L L Н L Н Н L L L Н Н Н Н Н L Н Н Н Н Н L L Н L L Н Н Н Н L Н Н Н Н Н н н L L н L н н н L н н Н Н Н L L Н L Н Н Н Н Н L Н Н L Н Н Н Н Н Н Н Н Н L Н L

H: High level

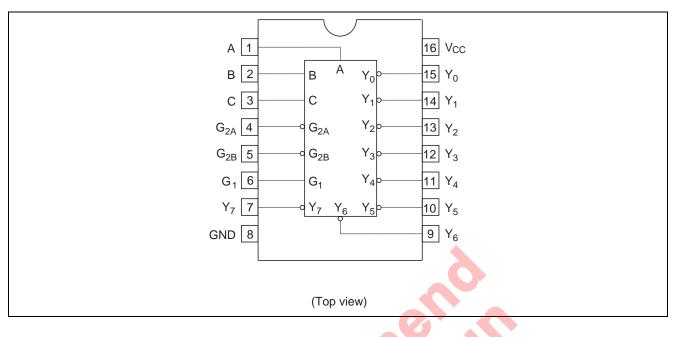
L: Low level

X: Immaterial



#### HD74LVC138

### **Pin Arrangement**



# **Absolute Maximum Ratings**

Item	Symbol	Ratings		Unit	Conditions
Supply voltage	V <sub>CC</sub>	–0.5 to 6.0		V	
Input diode current	I <sub>IK</sub>	-50	XV	mA	$V_1 = -0.5 V$
Input voltage	VI	-0.5 to 6.0	0	V	
Output diode current	I <sub>ОК</sub>	-50		mA	$V_0 = -0.5 V$
		50		_	$V_{\rm O} = V_{\rm CC} + 0.5 \ V$
Output voltage	Vo	–0.5 to V <sub>CC</sub> +0.5		V	
Output current	Ι <sub>Ο</sub>	±50		mA	
V <sub>CC</sub> , GND current / pin	I <sub>CC</sub> or I <sub>GND</sub>	100		mA	
Storage temperature	Tstg	-65 to +150		°C	

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.



### **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	Vcc	1.5 to 5.5	V	Data retention
		2.0 to 5.5		At operation
Input / output voltage	VI	0 to 5.5	V	G, A, B, C
	Vo	0 to V <sub>CC</sub>	V	Y <sub>0</sub> to Y <sub>7</sub>
Operating temperature	Та	-40 to 85	۵°	
Output current	I <sub>ОН</sub>	-12	mA	$V_{CC} = 2.7 V$
		-24 <sup>*2</sup>		$V_{CC} = 3.0 \text{ V} \text{ to } 5.5 \text{ V}$
	I <sub>OL</sub>	12	mA	$V_{CC} = 2.7 V$
		24 <sup>*2</sup>		$V_{CC} = 3.0 \text{ V} \text{ to } 5.5 \text{ V}$
Input rise / fall time *1	t <sub>r</sub> , t <sub>f</sub>	10	ns/V	

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

2. Duty cycle  $\leq 50\%$ 

### **Electrical Characteristics**

			Ta = -40 to 85°C				
Item	Symbol	V <sub>cc</sub> (V)	Min	Max	Unit	Test Conditions	
Input voltage	V <sub>IH</sub>	2.7 to 3.6	2.0	-	V		
		4.5 to 5.5	V <sub>CC</sub> ×0.7		6		
	VIL	2.7 to 3.6	-	0.8	V		
		4.5 to 5.5	-	V <sub>cc</sub> ×0.3	0		
Output voltage	V <sub>OH</sub>	2.7 to 5.5	V <sub>cc</sub> -0.2	-0	V	I <sub>OH</sub> = -100 μA	
		2.7	2.2	+	_	$I_{OH} = -12 \text{ mA}$	
		3.0	2.4	—	_		
		3.0	2.0	<u> </u>	_	$I_{OH} = -24 \text{ mA}$	
		4.5	3.8	_	_		
	V <sub>OL</sub>	2.7 to 5.5	-	0.2	V	l <sub>OL</sub> = 100 μA	
•		2.7	-	0.4	_	I <sub>OL</sub> = 12 mA	
		3.0	—	0.55	_	I <sub>OL</sub> = 24 mA	
		4.5	—	0.55	_		
Input current	I <sub>IN</sub>	0 to 5.5	_	±5.0	μA	$V_{IN} = 5.5 \text{ V or GND}$	
Quiescent supply current	Icc	5.5		20	μA	$V_{IN} = V_{CC}$ or GND	
	$\Delta I_{CC}$	3.0 to 3.6		500	μA	$V_{IN}$ = one input at( $V_{CC}$ –0.6)V, other inputs at $V_{CC}$ or GND	

## **Switching Characteristics**

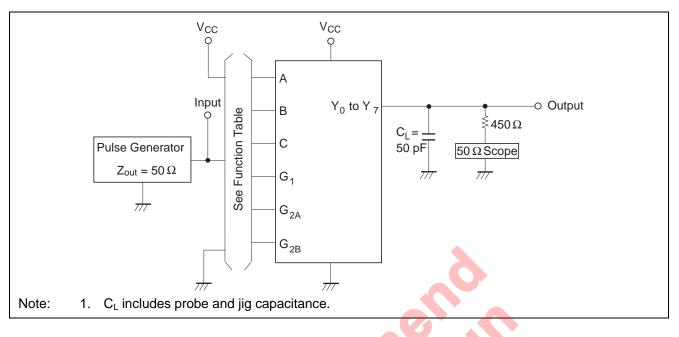
			Ta = -40 to 85°C				From	То
Item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Unit	(Input)	(Output)
Propagation delay time	t <sub>PLH</sub>	2.7	_	7.0	10.0	ns	G, A, B, C	$Y_0$ to $Y_7$
	<b>t</b> PHL	3.3±0.3	1.5	5.0	9.0			
		5.0±0.5	_	3.5	7.5			
Input capacitance	CIN	2.7	_	3.0	_	pF		
Output capacitance	Co	2.7	_	15.0		pF		

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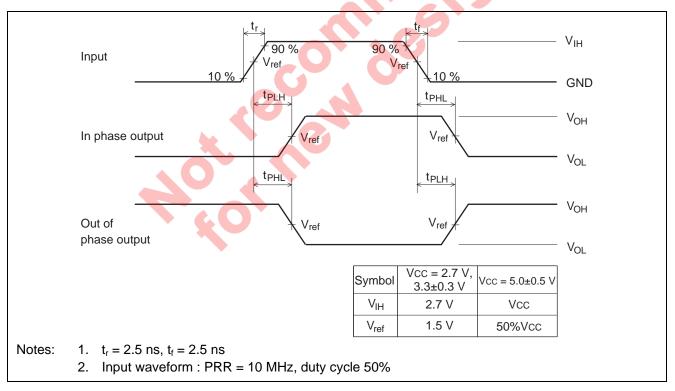


### HD74LVC138

### **Test Circuit**

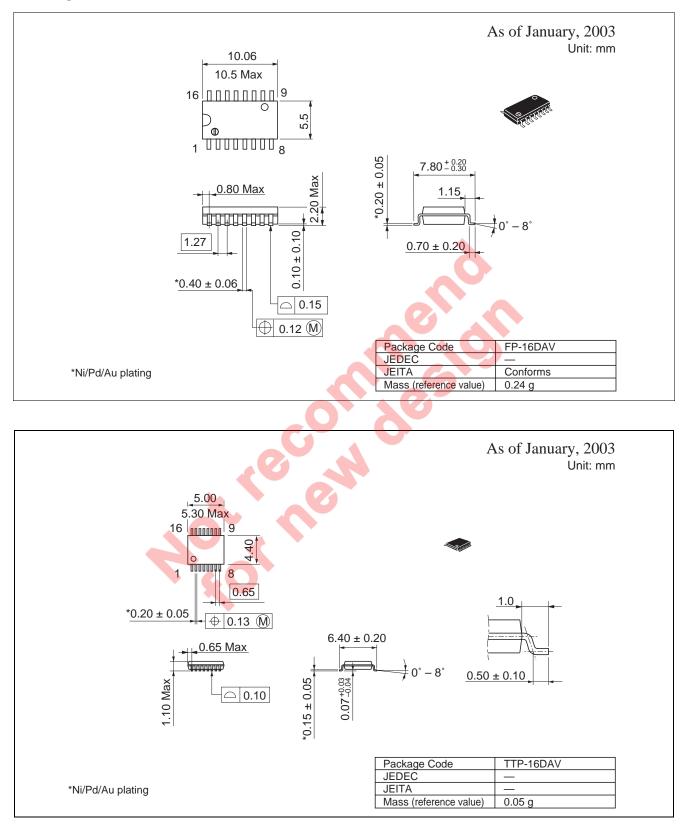


Waveforms



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### **Package Dimensions**



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